



Ravindra College of Engineering for Women

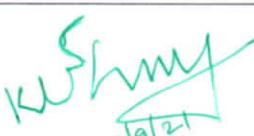
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ACADEMIC YEAR – 2020 – 2021

LIST OF ICT TOOLS

DEPARTMENT OF COMPUTER SCIENCE ENGINEERING (AI)

S.No	Class	Topic Delivered using ICT	Subject	<i>ICT Resource</i>
1	I	Python Functions	Python Programming & Data Science	https://www.w3schools.com/python/python_functions.asp
2	I	Strings	Python Programming & Data Science	https://www.guru99.com/python-tutorials.html
3	I	Python Exception Handling	Python Programming & Data Science	https://www.geeksforgeeks.org/python-programming-language/learn-python-tutorial/#exception
4	I	Python programming examples	Python Programming & Data Science	https://www.geeksforgeeks.org/python-programming-examples/
5	I	Data Visualization in Python	Python Programming & Data Science	https://www.analyticsvidhya.com/blog/2021/02/an-intuitive-guide-to-visualization-in-python/
6	I	Storage classes	C & DS	https://www.youtube.com/IRRPO4C0suQ
7	I	Pointers& Functions	C & DS	https://www.youtube.com/LuDJeGqEZ3A
8	I	Command Line Arguments	C & DS	https://www.youtube.com/Ww1yFSz8SDg
9	I	Looping structures	C & DS	https://www.youtube.com/bPfu5IXnEI8
10	I	stacks and queues	C & DS	https://www.youtube.com/DKIXEAZYB71


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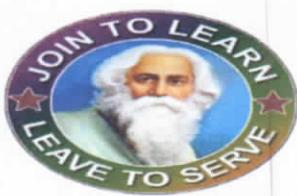
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11	I	Linked Lists & Its Operations	C & DS	https://www.youtube.com/DKIXEAZYB71
12	I	Trees	C & DS	https://www.youtube.com/YAdLFsTG70w
13	I	Graphs	C & DS	https://www.youtube.com/xwlRWdaHMBg
14	I	Binary Search	C & DS	https://www.youtube.com/V_T5NuccwRA

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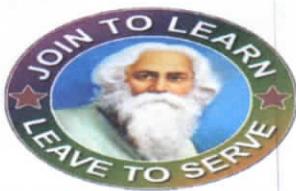
2.3.2. The list of *ICT resources used for effective teaching-learning process.*

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

AY : 2020-21

S.No	Class	Topic Delivered using ICT	Subject	<i>ICT resource</i>
1	III	Basic Maxwell's equations	AWP	https://www.youtube.com/watch?v=XUR-dnDa7eI
2	III	Antenna Arrays	AWP	https://www.youtube.com/watch?v=mudiXpU_DVQ
3	III	Pulse code modulation	DCS	https://www.youtube.com/watch?v=l_SqkcP6hZ4
4	III	Introduction Convolution Codes	DCS	https://www.youtube.com/watch?v=AnyVu5eDhAQ
5	III	FET	DSP	https://www.youtube.com/watch?v=r4Pc-rGBRJA
6	III	Discrete Time Signal & system	DSP	https://www.youtube.com/watch?v=8zbBJkw5KsM&list=PL4FA894BD6A9586E1
7	III	Logic Gates	VLSI	https://www.youtube.com/watch?v=fT3TC2AU_H8
8	III	Design of Adders	VLSI	https://www.youtube.com/watch?v=85XxQZqBNlg https://www.youtube.com/watch?v=YBY0k5mlQy0
9	IV	Photo Detector	OFC	https://www.youtube.com/watch?v=1X2Xt7wlcbA
10	IV	Attenuation & Power loss in fibers	OFC	https://www.youtube.com/watch?v=FVHzrGRN6i8
11	IV	Gunn diode, Varactor diode	MWE	https://www.youtube.com/results?search_query=Gunn+diode-principles+classes+by+iit+professors
12	IV	Directional Couplers	MWE	https://www.youtube.com/watch?v=44mBn7nyMIM
13	IV	Edge Detection	DIP	https://www.youtube.com/watch?v=9r8ph2pb9aw
14	IV	Discrete Cosine Transforms	DIP	https://www.youtube.com/watch?v=S8FkaEWfCOg
15	IV	Transmission media reflection	RFIC	https://www.youtube.com/watch?v=q9k9FPh0iJl&list=PLbMVogVj5nJQdGDSx243YPnNeLMBrhNE8&index=2
16	IV	RF system basic architectures	RFIC	https://www.youtube.com/watch?v=KUDGGsyh1Hs

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S.No	Class	Topic Delivered using ICT	Subject	ICT resource
17	II	Sample space & Events	PTSP	https://www.youtube.com/watch?v=leVm6xuKdlU
18	II	Random Variables	PTSP	https://www.youtube.com/watch?v=zugeSyREcQ4
19	II	Zener Diode and applications	EDC	https://www.youtube.com/watch?v=l2nrBITEVls
20	II	BJT biasing- methods	EDC	https://www.youtube.com/watch?v=l6M6FvjUdTI
21	II	Vestigial sideband (VSB) modulator	ACS	https://www.youtube.com/watch?v=KC8kh5FaIc0
22	II	Amplitude modulation	ACS	https://www.youtube.com/watch?v=UznnkHMislk
23	II	Transient response of second order systems	CSE	https://www.youtube.com/watch?v=RJaQkFE3HQA
24	II	Mason's gain formula	CSE	https://www.youtube.com/watch?v=au7rDksP5nQ

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Quiz Summary

[Section Filter ▾](#)
[Student Analysis](#)
[Item Analysis](#)

Average Score

52%

High Score

95%

Low Score

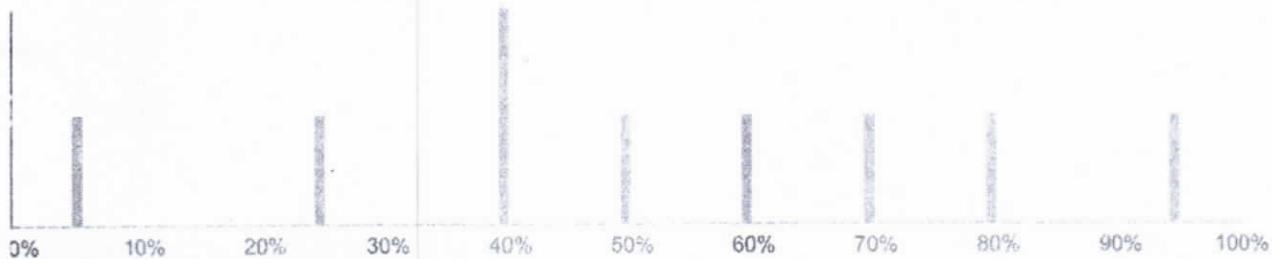
5%

Standard Deviation

5.27

Average Time

15:55:37



Question Breakdown

Attempts: 9 out of 9

1. Decoder with enable input is called as _____

+0.42

Discrimination

Index

demultiplexer	2 respondents	22 %
multiplexer		0 %
Encoder	7 respondents	78 %
none		0 %

KRemy 131921
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22%
answered
correctly

Attempts: 8 out of 9

2.The sum of two binary bits with previous carry is said to be _

+0.3

Discrimination
Index

Full adder	7 respondents	78 %	✓
Half adder		0 %	
both a & b	1 respondent	11 %	
either a or b		0 %	
No Answer	1 respondent	11 %	

78%
answered
correctly

Attempts: 9 out of 9

3.A combinational logic gate composed of OR and NOT gate is _

+0.83

Discrimination
Index

NOR	4 respondents	44 %	RAVINDRA COLLEGE OF ENGINEERING FOR WOMEN Pasupula(V), Nanchangur Road, Near Venkayapalli, KURNool-518 002	✓
NAND	2 respondents	22 %		
XOR	2 respondents	22 %		

XNOR 1 respondent

11 %

44%
answered
correctly

Attempts: 9 out of 9

4.A full subtractor can be implemented by using two half subtractions.

-0.27

Discrimination

Index 

True 7 respondents

78 %

✓78%

False 2 respondents

22 %

answered

correctly

Attempts: 8 out of 9

5.Data Selector is also called as _____

+0.27

Discrimination

Index 

multiplexer 3 respondents

33 %

✓

Encoder 2 respondents

22 %

demultiplexer

0 %

NONE 3 respondents

33 %

*Lucky
13/11/24*

No Answer 1 respondent

11 %

Search for assignment + Group + Assignment (<https://canvas.instructure.com/courses/1892770/assignments/new>) ... ▾ Assignments + **M JYOTHIRMAI**

- [\(https://canvas.instructure.com/courses/1892770/assignments/14164433\)](https://canvas.instructure.com/courses/1892770/assignments/14164433)
- Due 2 Apr 2020 at 23:59 | -/100 pts

assignment

- [\(https://canvas.instructure.com/courses/1892770/assignments/14327866\)](https://canvas.instructure.com/courses/1892770/assignments/14327866)
- /100 pts

unit test 1

- [\(https://canvas.instructure.com/courses/1892770/assignments/14559012\)](https://canvas.instructure.com/courses/1892770/assignments/14559012)
- /100 pts

K Srinivas
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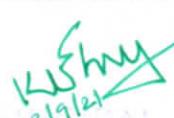
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Search...



Student name	unit test 1 (https://canvas.instructure.com/courses/14803770/assignments/100)	assignment (https://canvas.instructure.com/courses/14803770/assignments/100)	M JYOTHIRMAI (https://canvas.instructure.com/courses/14803770/assignments/100)
ace.haviya1@gmail...	100	-	
anjana163t1a0473...	100	-	
arshiya121199@gmail...	100	-	
bniharika407@gmail...	100	-	
charithaanukali123...	100		
chitralavasavi007@gmail...	100	-	
eshwarammalakka...	100		
fathimullahathiya41...	100	-	
fmsaleela418@gmail...	100	-	
gundanavya423@gmail...	100	-	
jabeenakausar406...	100		
k.sirisha442@gmail...	100		
kaipagouthami@gmail...	100	-	
ommeharshitha@gmail...	100		
krishnasreekarana...	100		
madhurim550@gmail...	100		
manasabutta409@gmail...	100	-	
mereddybhavana34...	100	-	
msarithareddy03@gmail...	100		
nandyalajyothi460...	100		


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ACADEMIC YEAR – 2020 – 2021

LIST OF ICT TOOLS

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

S.No	Class	Topic Delivered using ICT	Subject	ICT Resource
1	IV	Block cipher modes of operation	Information Security	PPT
2	II	Inheritance	OOP (JAVA)	PPT
3	III	Data Flow Analysis	CD	http://nptel.ac.in/courses/106108052/27
4	IV	Android Developing Environment	MC	http://nptel.ac.in/courses/106106147/10
5	III	Taxonomy Of Bugs	Software Testing	https://youtu.be/tWEMRikSyZs
6	IV	OLAP Technology for Data Mining	DWDM	https://youtu.be/g9gj8lhhguvw
7	III	State Chart Diagram	OOAD	https://youtu.be/YnFJ3ZNx0Nk
8	II	Turing Machine	Python Programming	http://nptel.ac.in/courses/106104148/26
9	IV	Design Pattern Relationship	Software Architecture & Design Patterns	PPT
10	III	DNS	Computer Networks	PPT
11	IV	RSA Algorithm	CNS	http://nptel.ac.in/courses/106105031/28
12	II	Overview of a Database Management System	DBMS	http://nptel.ac.in/courses/106106095/1
13	III	Construct dynamic WebPages using DHTML	WT	PPT
14	IV	Path Instrumentation	ST	https://youtu.be/fMQ0Oh61K2c
15	III	Agile development	Software Engineering	http://nptel.ac.in/courses/106101061/26
16	II	DLL	Data Structures	http://nptel.ac.in/courses/106106130/7
17	II	Graphs	Discrete Mathematics	PPT
18	III	Hadoop file sys	BDT	https://youtu.be/GJYEsEEfjk
19	IV	Introduction to Android studio	Mobile Application Development	http://nptel.ac.in/courses/106106147/6
20	IV	Modelling Timing Constraints	RTS	http://nptel.ac.in/courses/106105036/4



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21	III	Operating system structure	OPERATING SYSTEMS	http://nptel.ac.in/courses/106106144/2
22	II	FLIP FLOPS	CO	https://youtu.be/68djlnc5jec
23	IV	COMPONENTS OF PATTERN RECOGNITION	PATTERN RECOGNITION	https://youtu.be/_JSwIRWaY8
24	II	Turing Machine	FLAT	http://nptel.ac.in/courses/106104148/26
25	III	Kernel data Structures	OS	https://youtu.be/fUg2jFXSsHE
26	IV	DNS	Computer Networks and Security	https://youtu.be/AvvkvKJ2Jas
27	III	Unix commands	Unix and Shell Programming	https://youtu.be/E002zP3E25M
28	II	DMA	Computer organization	PPT
29	III	Agile development	Software Engineering	http://nptel.ac.in/courses/106101061/26
30	IV	IP V6& V4	Internetworking with TCP/IP	https://youtu.be/FsieCc0SWn0
31	IV	Slicing, Methods	PYTHON	https://youtu.be/WWso6gVuKpI
32	III	FLOW GRAPHS AND PATH TESTING	STM	https://youtu.be/D96aTopM1T4
33	III	System Modelling Concepts	SE	http://nptel.ac.in/courses/106101061/8
34	IV	Android Developing Environment	MC	http://nptel.ac.in/courses/106106147/10
35	II	Algebraic Structures	Discrete Mathematics	http://nptel.ac.in/courses/106106094/35
36	II	The Binomial Theorem	Discrete Mathematics	https://youtu.be/pQbPhZbpM10
37	II	NP completeness	Design And Analysis of Algorithms	http://nptel.ac.in/courses/106101060/26

K Remya Balaji

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Academic Year: 2019-20
DEPARTMENT OF CSE

Teachers Using ICT

S.No	Name Of The Teacher	Class	Topic Delivered using ICT	Subject	ICT tools used
1	K Mahesh Babu	IV CSE A & B	J2ME Architecture	Mobile Application Development	PPT
2	K Mahesh Babu	II CSE A & B	INHERITANCE	OOP (JAVA)	PPT
3	P Ragha Vardhani	III CSE A & B	Data Flow Analysis	CD	http://nptel.ac.in/courses/106108052/27
4	B Ravi	IV CSE A & B	Android Developing Environment	MC	http://nptel.ac.in/courses/106106147/10
5	Y. Indira Priyadarshini	III CSE A & B	Transaction flow testing	Software Testing	PPT
6	K Vijay Kumar	III CSE A & B	OLAP Technology for Data Mining	DWDM	PPT
7	S Nuzhath Pasha	III CSE A & B	State Machine Diagram	OOAD	https://youtu.be/YnFJ3ZNx0Nk
8	K Vinod Kumar Reddy	II CSE A & B	Turing Machine	FLAT	http://nptel.ac.in/courses/106104148/26
9	S Aslam Shareef	IV CSE A & B	Behavioral Patterns	Software Architecture & Design Patterns	PPT
10	Y.Indira Priyadarshini	III CSE A & B	Path Testing	Software Testing	PPT
11	B Naresh Achari	IV CSE A & B	RSA Algorithm	CNS	http://nptel.ac.in/courses/106105031/28
12	P Prathusha	II CSE A & B	Overview of a Database Management System	DBMS	http://nptel.ac.in/courses/106106095/1
13	M Kiranmai	III CSE A & B	SERVLET LIFECYCLE	WT	PPT
14	G Fayaz Hussain	IV CSE A & B	taxonomy of bugs	SPM	PPT
15	G Fayaz Hussain	III CSE A & B	Agile development	Software Engineering	http://nptel.ac.in/courses/106101061/26
16	K. Nageswari	II CSE A & B	DLL	Data Structures	http://nptel.ac.in/courses/106106130/7
17	M Anantha Lakshmi	II CSE A & B	Graphs	Discrete Mathematics	PPT
18	S Amulya	III CSE A & B	HADOOP FILE SYS	BDT	PPT
19	K Mahesh Babu	IV CSE A & B	Introduction to Android studio	Mobile Application Development	http://nptel.ac.in/courses/106106147/6
20	K Shouryadar	IV CSE A & B	Modeling Timing Constraints	RTS	http://nptel.ac.in/courses/106105036/4
21	M Kiranmayee	III CSE A & B	Operating system structure	OPERATING SYSTEMS	http://nptel.ac.in/courses/106106144/2
22	P Prathusha	II CSE A & B	Index Structures	DBMS	PPT
23	K Shouryadar	IV CSE A & B	Characteristics of RTS	Real Time Systems	PPT
24	K Vinod Kumar Reddy	II CSE A & B	Turing Machine	FLAT	http://nptel.ac.in/courses/106104148/26
25	M Kiranmayee	III CSE	Kernel data Structures	OS	https://youtu.be/fUg2jFXSsHE

		A & B			
26	G Fayaz Hussain	IV CSE A & B	FTP,SMTP	Computer Networks and Security	http://nptel.ac.in/courses/106105081/39
27	S Nuzhath Pasha	III CSE A & B	Unix structure	Unix and Shell Programming	PPT
28	P Surendra	II CSE A & B	DMA	Computer organization	PPT
29	G Fayaz Hussain	III CSE A & B	Agile development	Software Engineering	http://nptel.ac.in/courses/106101061/26
30	G Fayaz Hussain	IV CSE A & B	UDP	Internetworking with TCP/IP	https://youtu.be/XIEehqcL5Lo
31	M Kiranmayee	III CSE A & B	Deadlocks	OPERATING SYSTEMS	http://nptel.ac.in/courses/106106144/32
32	K Vinod Kumar	II CSE A & B	Slicing, Methods	PYTHON	https://youtu.be/WWs06gVuKpI
33	Y.Indira Priyadarshini	III CSE A & B	FLOGRAPHS AND PATH TESTING	ST	PPT
34	S Aslam Shareef	IV CSE A & B	architecture of Vector Super computers	Advanced Computer Architecture	PPT
35	K Mahesh Babu	IV CSE A & B	Screen Designing	HCI	PPT
36	G Fayaz Hussain	III CSE A & B	System Modeling Concepts	SE	http://nptel.ac.in/courses/106101061/8
37	B Ravi	IV CSE A & B	Android Developing Environment	MC	http://nptel.ac.in/courses/106106147/10
38	M Anantha Lakshmi	II CSE A & B	Algebraic Structures	Discrete Mathematics	http://nptel.ac.in/courses/106106094/35
39	P Surendra	II CSE A & B	Hardwired Control	Computer organization	PPT
40	M Anantha Lakshmi	II CSE A & B	OLAP	DM	PPT
41	M Anantha Lakshmi	II CSE A & B	The Binomial Theorem	Discrete Mathematics	https://youtu.be/pQbPhZbpM10
42	S Aslam Shareef	IV CSE A & B	Structural Pattern	Software Architecture & Design Patterns	https://youtu.be/lPsSL6_7NBg
43	S Amulya	III CSE A & B	HADOOP ECO ECTURSYSTEURMS	BDT	PPT
44	M Anantha Lakshmi	II CSE A & B	NP completeness	Design And Analysis of Algorithms	http://nptel.ac.in/courses/106101060/26

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ACADEMIC YEAR – 2019 – 2020

LIST OF ICT TOOLS

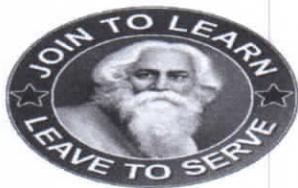
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

S.No	Class	Topic Delivered using ICT	Subject	ICT Resource
1	III	M-Aryay PAM	DCS	https://www.youtube.com/watch?v=YkWbfQx9yzI
2	III	Introduction to	DCS	https://www.youtube.com/watch?v=LYWCI2XvXrc
3	III	Shift Registers	DSD	https://www.youtube.com/watch?v=lecj9xmlfXM
4	III	Multiplexers	DSD	https://www.youtube.com/watch?v=JjQNIikKnxpA
5	III	Addressing modes	MPMC	https://www.youtube.com/watch?v=vu6eLFmrwaA
6	III	RISC instruction	MPMC	https://www.youtube.com/watch?v=bkjqOKGQuNY
7	III	Design flow of VLSI circuit	VLSI	https://www.youtube.com/watch?v=7NcKhTYOMMQ
8	III	Parity Generator	VLSI	https://www.youtube.com/watch?v=6EstVLJxwW8
9	IV	Fundamentals of Embedded	Embedded Systems	https://www.youtube.com/watch?v=hELr9-7aAG8
10	IV	ARM Architecture	Embedded	https://www.youtube.com/watch?v=4VRtujwa_b8
11	IV	Doppler Effect	Radar	https://www.youtube.com/watch?v=fvQrbtRCH8I
12	IV	Overview of	Radar	https://www.youtube.com/watch?v=HYAyfBCKV4Y
13	IV	Dyadic Multi-resolution Analysis	Advanced Digital Signal Processing-	https://www.youtube.com/watch?v=ToHnKluvlk
14	IV	Haar Wavelet	Advanced Digital Signal Processing-	https://www.youtube.com/watch?v=HEUhSbD4P5c
15	IV	RF system basic	RFIC	https://www.youtube.com/watch?v=KUDGGsyh1Hs
16	IV	Phase locked loop	RFIC	https://www.youtube.com/watch?v=XNWY857rdQo
17	II	Distribution & Density Function	PTSP	https://www.youtube.com/watch?v=EbxKvaAO9J4
18	II	Central Limit	PTSP	https://www.youtube.com/watch?v=6IODTEWWV70
19	II	Inverse Z-	SS	https://www.youtube.com/watch?v=Uc5SFRp6ex0
20	II	Discrete time	SS	https://www.youtube.com/watch?v=Q8wuqYsdnSs
21	II	RC coupled	ECA	https://www.youtube.com/watch?v=BSR26SU3R2U
22	II	Large Signal	ECA	https://www.youtube.com/watch?v=qjWJXOKTegY
23	II	Step- Impulse	CSE	https://www.youtube.com/watch?v=Ar48Stzr8E
24	II	Transition Matrix and it's Properties	CSE	https://www.youtube.com/watch?v=4yb-Qdd6gzY

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10/10/19

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RAVINDRA COLLEGE OF
ENGINEERING FOR WOMEN
Pasupula(V), Nanikotkur Road,
Near Venkayapalli, KURNNOOL-518 003

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COLLEGE OF ENGINEERING FOR WOMEN

Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuramu
Nandikotkur Road, Pasupula Village, Venkayapalli, Kurnool - 518452, Andhra Pradesh
(An ISO 9001:2008 Certified Institution)

ACADEMIC YEAR – 2018 – 2019

LIST OF ICT TOOLS

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

S.N o	Clas s	Topic Delivered using ICT	Subject	ICT resource
1	III	Class diagram	OOAD	https://youtu.be/UI6lqHOVHic
2	I	Stacks	Data structures	https://youtu.be/g1USSZVWDsY
3	II	8086 Architecture	MPMC	PPT
4	IV	Introduction to Android studio	Mobile Application Development	http://nptel.ac.in/courses/106106147/6
5	III	HDLC	Computer Networks	PPT
6	II	Algebraic Structures	Discrete Mathematics	http://nptel.ac.in/courses/106106094/35
7	IV	J2ME best practices, patterns	Mobile Application Development	PPT
8	III	PPP	Computer Networks	PPT
9	II	Lock Protocols	Database Management systems	https://youtu.be/a74V14OnDvw
10	I	Quick sort	Data structures	https://youtu.be/gtWw_8VvHjk
11	IV	Control structures	PHYTTHON	http://nptel.ac.in/courses/115104095/4
12	II	Hardwired Control	Computer organization	PPT
13	III	DHTML	WT	https://youtu.be/HTFSIJALNKc
14	IV	IPV6	Mobile Computing	https://youtu.be/FRT0l_Hi0
15	III	Advanced Features	OOAD	PPT
16	II	Inheritance	OOP (JAVA)	PPT
17	III	Sequence Diagram	OOAD	https://youtu.be/pCK6prSq8aw
18	IV	HTTP,SHTTP	Computer Networks and Security	http://nptel.ac.in/courses/106105081/40
19	III	Transaction flow testing	Software Testing	PPT
21	IV	Python packages	PYTHON	PPT
22	III	Run Time Environments	CD	http://nptel.ac.in/courses/106108113/21
23	IV	Dynamic SQL	Mobile Application Development	PPT

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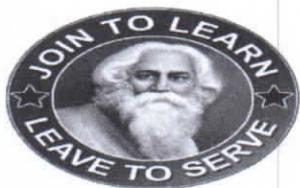
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S.N o	Cla ss	Topic Delivered using ICT	Subject	ICT resource
24	III	Agile development	Software Engineering	http://nptel.ac.in/courses/106101061/26
25	II	DFA Minimization	FLAT	http://nptel.ac.in/courses/106104148/14
26	IV	J2ME Overview	Mobile Application Development	PPT
27	III	FLOWGRAPHS AND PATH TESTING	STM	PPT
28	II	DMA	Computer organization	PPT
29	III	Phases of a compiler	CD	http://nptel.ac.in/courses/106108113/1
30	IV	Security and System Permissions	MC	http://nptel.ac.in/courses/106106147/30
31	II	Concurrency control	Database Management systems	https://nptel.ac.in/courses/106/105/106105175/
32	I	AVL trees	Data structures	https://youtu.be/FNeL18KsWPc
33	III	Component Diagram	OOAD	PPT
34	III	Hadoop eco ectursysteurms	BDT	PPT
35	IV	Fragments- Advanced Example	MC	http://nptel.ac.in/courses/106106147/26
36	II	Graphs	Discrete Mathematics	PPT
37	I	Introduction to hash tables	Data structures	https://youtu.be/shs0KM3wKv8
38	IV	architecture of Vector Super computers	Advanced Computer Architecture	PPT
39	III	IPV4 Addresses.	CN	http://nptel.ac.in/courses/106105081/28
40	IV	Basic java	Mobile Application Development	https://youtu.be/Bz0aw4_K8oc?list=PL49hKkxjHVqHilv8CUb-p9gMPFZtfqZTN
41	IV	Behavioural Patterns	Software Architecture & Design Patterns	PPT
42	III	USE CASE DIAGRAMS	OOAD	PPT
43	IV	Android Development Environment	Mobile Application Development	http://nptel.ac.in/courses/106106147/10
44	II	NORMAL FORMS	Database Management Systems	http://nptel.ac.in/courses/106106093/9

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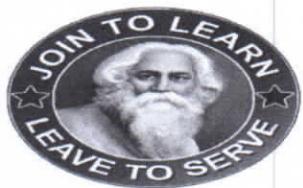
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ACADEMIC YEAR – 2018 – 2019

LIST OF ICT TOOLS

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

S.N o	Class	Topic Delivered using ICT	Subject	ICT resource
1	III	Differential amplifier	LICA	https://www.youtube.com/watch?v=LjGB9oj3WIQ
2	III	Tow-Thomas biquad filter	LICA	https://www.youtube.com/watch?v=luWwG8-B4Pg
3	III	Dipole, loop, monopole antenna's introduction	AWP	https://www.youtube.com/watch?v=wx_tIvaajAI
4	III	Antenna measurements	AWP	https://www.youtube.com/watch?v=TFYObg9PpgU
5	III	8086 Microprocessor	MPMC	https://www.youtube.com/watch?v=GapjjO_8Kuk
6	III	Protocols (12c,SPI,UART)	MPMC	https://www.youtube.com/watch?v=U5CDF4TNARE
7	III	MOS Transistor	VLSI	https://www.youtube.com/watch?v=WBa2Fw5-yU4
8	III	NMOS Inverter and CMOS Inverter	VLSI	https://www.youtube.com/watch?v=zNqmhJHDwc
9	IV	Embedded Hardware	Embedded Systems	https://www.youtube.com/watch?v=C04ZthY8Yqk



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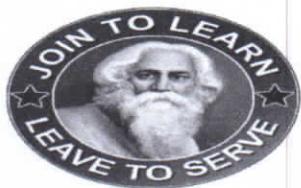
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S.No	Class	Topic Delivered using ICT	Subject	ICT resource
10	IV	Addressing modes & instructions	Embedded Systems	https://www.youtube.com/watch?v=vu6eLFmrwAA
11	IV	Doppler Effect	Radar Systems	https://www.youtube.com/watch?v=fvQrbtRCH8I
12	IV	Overview of Radar	Radar Systems	https://www.youtube.com/watch?v=HYAyfBCKV4Y
13	IV	IIR Filter Design	Advanced Digital Signal Processing-Multirate& Wavelet	https://www.youtube.com/watch?v=3QWvi8EC_DI
14	IV	Conjugate Quadrature Filter Banks (CQF) and their design	Advanced Digital Signal Processing-Multirate& Wavelet	https://www.youtube.com/watch?v=3-zqF9-g7-U
15	IV	RF system basic architectures	RFIC	https://www.youtube.com/watch?v=KUDGGsyh1Hs
16	IV	Smith Chart	RFIC	https://www.youtube.com/watch?v=VzGnb2K4RGQ
17	II	Random Variables	PTSP	https://www.youtube.com/watch?v=zufeSyREcQ4
18	II	Joint Distribution Function	PTSP	https://www.youtube.com/watch?v=S_Zx-g8FU0w
19	II	Dirac delta Function	SS	https://www.youtube.com/watch?v=B0udyDBeXMw
20	II	Fourier Series	SS	https://www.youtube.com/watch?v=U8riFeiiu3s

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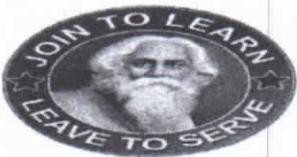
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S.No	Class	Topic Delivered using ICT	Subject	ICT resource
20	II	Fourier Series	SS	https://www.youtube.com/watch?v=U8riFeiu3s
21	II	Shannon's encoding algorithm	ACS	https://www.youtube.com/watch?v=utRt_XCi1Hg
22	II	Vestigial sideband (VSB) modulator	ACS	https://www.youtube.com/watch?v=KC8kh5FaIc0
23	II	stability – Routh's stability criterion	CSE	https://www.youtube.com/watch?v=cJRIUGDtS-0
24	II	Nyquist Plots	CSE	https://www.youtube.com/watch?v=8Rwiv13O4L4

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ACADEMIC YEAR 2018 – 2019

LIST OF ICT TOOLS

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

S.No	Class	Topic Delivered using ICT	Subject	ICT resource
1	III	Active Filters And Oscillators	Linear And Digital IC Applications	https://www.youtube.com/XzHLQt0H66Q
2	III	Micro Processors and Micro controllers	Micro Processors and Micro controllers	https://www.youtube.com/liRPtvj7bFU
3	II	Magneto statics	Electromagnetic Fields	https://www.youtube.com/Y6ngDf_f-04
4	II	BJT	Electronic Devices & Circuit	https://www.youtube.com/CZIMt2Ta68I
5	IV	Digital Signal Processing	Digital Signal Processing	https://youtu.be/yrWL90OAFg
6	IV	Embedded Systems	Embedded Systems	https://www.youtube.com/y9RAhEfLfJs
7	IV	Concentrating Collectors & Solar Collectors	Energy Resources & Technology	https://www.youtube.com/3s0sUiCaxuQ
8	IV	Design Cycle in the Development Phase	Embedded Systems	https://www.youtube.com/3s0sUiCaxuQ
9	IV	Methods Of Electrical Heating	Utilization Of Electrical Energy	https://youtu.be/y716ggS_Chc
10	III	Introduction- 8086 Architecture- Block Diagram,	microprocessors and microcontrollers	https://youtu.be/XX9rDGTBGgQ
11	III	Over Current Relay	power system protection	https://youtu.be/I4L9P64UGIw
12	III	Complimentary Symmetry Circuits	analog electronic circuits	https://www.youtube.com/WNOGAQ0ww44
13	III	Single Phase Half and Full Bridge Inverters- Pulse	power electronics	https://youtu.be/ApYWEI4DbvY
14	II	Coulomb's Law - Electric Field Intensity(IFI)	electromagnetic fields	https://youtu.be/0mGRAFYQTbM

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RAVINDRA COLLEGE OF ENGINEERING FOR WOMEN
 Approved by AICTE and Affiliated to JNTUA, Ananthapuramu,
Academic Year: 2017-18
DEPARTMENT OF CSE

Teachers Using ICT

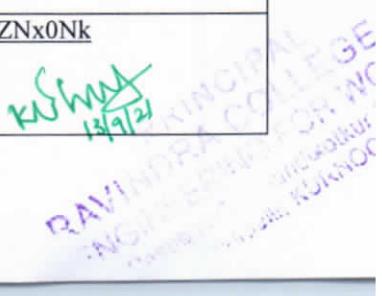
S.No	Name Of The Teacher	Class	Topic Delivered using ICT	Subject	ICT tools used
1	K Shouryadhar	IV CSE A & B	Mobile TCP	Mobile Computing	PPT
2	K Shouryadhar	IV CSE A & B	Network Protocols/Mobile IP	Mobile Computing	PPT
3	K Shouryadhar	IV CSE A & B	IPV6	Mobile Computing	https://youtu.be/FRT0l_Hii0
4	S Vinod Kumar	IV CSE A & B	Characteristics of RTS	Real Time Systems	PPT
5	M. Jyothi	II CSE A & B	8086 Architecture	MPMC	PPT
6	G. Fayaz Hussain	III CSE A & B	PPP	Computer Networks	https://youtu.be/kKCwkRT_U8I
7	G. Fayaz Hussain	III CSE A & B	HDLC	Computer Networks	PPT
8	S Vinod Kumar Reddy	I ECE - B	Iterative Statements	Computer Programming	http://nptel.ac.in/courses/106106127/7
9	G. Fayaz Hussain	II CSE A & B	Agile development	Software Engineering	http://nptel.ac.in/courses/106101061/26
10	K. Mahesh Babu	IV CSE A & B	J2ME Architecture	Mobile Application Development	PPT
11	Y. Indira Priyadarshini	III CSE A & B	Path Testing	Software Testing	PPT
12	K. Mahesh Babu	IV CSE A & B	Introduction to Android studio	Mobile Application Development	http://nptel.ac.in/courses/106106147/6
13	K. Mahesh Babu	IV CSE A & B	Android Development Environment	Mobile Application Development	http://nptel.ac.in/courses/106106147/10

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Academic Year: 2017-18
DEPARTMENT OF CSE

14	Y. Narasimhulu	III CSE A & B	Unix structure	Unix and Shell Programming	PPT
15	K. Mahesh Babu	IV CSE A & B	Android user interface	Mobile Application Development	http://nptel.ac.in/courses/106106147/11
16	M. Anantha Lakshmi	IV CSE B	DES	Computer Networks and Security	http://nptel.ac.in/courses/106105031/11
17	K Vinod Kumar Reddy	III CSE B	Class diagram	OOAD	https://youtu.be/UI6lqHOVHic
18	Y. Indira Priyadarshini	III CSE A & B	Transaction flow testing	Software Testing	PPT
19	K. Mahesh Babu	IV CSE A & B	Java interfaces	Mobile Application Development	http://nptel.ac.in/courses/106106147/4
20	K Vinod Kumar Reddy	III CSE B	Interaction diagram	OOAD	https://youtu.be/Ba7SyM78cUM
21	K. Mahesh Babu	IV CSE A & B	Basic java	Mobile Application Development	PPT
22	M. Anantha Lakshmi	IV CSE B	MAC, H-MAC	Computer Networks and Security	http://nptel.ac.in/courses/106105031/26
23	K. Mahesh Babu	IV CSE A & B	Embedded SQL	Mobile Application Development	PPT
24	K. Mahesh Babu	IV CSE A & B	Dynamic SQL	Mobile Application Development	PPT
25	K Vinod Kumar Reddy	III CSE B	Sequence Diagram	OOAD	https://youtu.be/pCK6prSq8aw
26	K. Mahesh Babu	IV CSE A & B	J2ME best practices, patterns	Mobile Application Development	PPT
27	M. Anantha Lakshmi	IV CSE B	AES	Computer Networks and Security	http://nptel.ac.in/courses/106105031/12
28	K Shouryadhar	III CSE A & B	OLAP	DM	PPT
29	T Deepa	II CSE A & B	Index Structures	DBMS	PPT
30	T Deepa	II CSE A	Concurrency control	Database Management systems	https://nptel.ac.in/courses/106/105/106105175/
31	K Vinod Kumar Reddy	III CSE A & B	State Machine Diagram	OOAD	https://youtu.be/YnFJ3ZNx0Nk

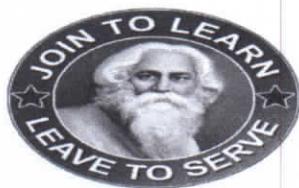

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32	K. Mahesh Babu	IV CSE A & B	J2ME Overview	Mobile Application Development	PPT
33	T Deepa	II CSE A & B	Lock Protocols	Database Management systems	https://youtu.be/a74V14OnDvw
34	K. Mahesh Babu	IV CSE A & B	Midlet suite	Mobile Application Development	PPT
35	K Vinod Kumar Reddy	III CSE A & B	Component Diagram	OOAD	PPT
36	K. Vinod Kumar Reddy	II CSE A & B	Turing Machine	FLAT	http://nptel.ac.in/courses/106104148/26
37	M. Anantha Lakshmi	IV CSE A & B	FTP,SMTP	Computer Networks and Security	http://nptel.ac.in/courses/106105081/39
38	K Vinod Kumar Reddy	III CSE A & B	UML architecture	OOAD	PPT
39	K Shouryadhar	II CSE A & B	Graphs	Discrete Mathematics	PPT
40	K Vinod Kumar Reddy	III CSE A & B	Interaction and sequence	OOAD	https://youtu.be/9VxmcTu48LQ
41	K Vinod Kumar Reddy	III CSE A & B	Advanced Features	OOAD	PPT


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ACADEMIC YEAR – 2017 – 2018

LIST OF ICT TOOLS

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

S.No	Class	Topic Delivered using ICT	Subject	ICT resources
1	III	Operational Amplifier	LICA	https://www.youtube.com/watch?v=clTA0pONnMs
2	III	Digital-to-Analog Converter & Analog-to-Digital Converter	LICA	https://www.youtube.com/watch?v=kMGap-0XwGs https://www.youtube.com/watch?v=TirvWsbqBUc
3	III	Transistor Transistor Logic (TTL)	DSD	https://www.youtube.com/watch?v=ne2WK5Vpg8k
4	III	Encoders & Decoders	DSD	https://www.youtube.com/watch?v=RZQTTfU9TN_A
5	III	Introduction on VLSI Design	VLSI	https://www.youtube.com/watch?v=9SnR3M3Clm4
6	III	Design of Adders	VLSI	https://www.youtube.com/watch?v=85XxQZqBNlq https://www.youtube.com/watch?v=YBY0k5mIQy0 https://www.youtube.com/watch?v=6E_cAf0Keng
7	III	Introduction on Digital Signal Processing	DSP	https://www.youtube.com/watch?v=6dFn pz_AEyA&list=PL9567DFCA3A66F299
8	III	Discrete Time Signal & system	DSP	https://www.youtube.com/watch?v=8zbBJkw5KsM&list=PL4FA894BD6A9586E1
9	IV	Introduction on Embedded Systems	Embedded Systems	https://www.youtube.com/watch?v=MkXydhCPnY_M&list=PLp6ek2hDcoNAxTQ7uyp68N_RpuULV-zrX&index=2
10	IV	Embedded System Design with ARM	Embedded Systems	https://www.youtube.com/watch?v=0xgvINDxXJl&list=PLbRMhDVUMngcJu5oUhgpgYqtOn7DmSfuU
11	IV	Introduction to waveguides & rectangular waveguide	Microwave Engineering	https://www.youtube.com/watch?v=k-zeCttEYVQ

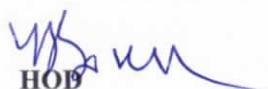


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S.No	Class	Topic Delivered using ICT	Subject	ICT resources
12	IV	Introduction to Microwave Engineering	Microwave Engineering	https://www.youtube.com/watch?v=bi1nDg9CqRo
13	IV	Introduction to ADSP-MM	Advanced Digital Signal Processing-Multirate& Wavelet	https://www.youtube.com/watch?v=4ufeTZ6fSNY&list=PLbMVogVj5nJRY7X-tMNDHPGdmfZyfHC7J
14	IV	Multiresolution analysis	Advanced Digital Signal Processing-Multirate& Wavelet	https://www.youtube.com/watch?v=BDa-5fCXJS4&list=PLbMVogVj5nJRY7X-tMNDHPGdmfZyfHC7J&index=3
15	IV	Transmission media reflection	RFIC	https://www.youtube.com/watch?v=q9k9FPh0iJI&list=PLbMVogVj5nJQdGDSx243YPnNeLMBrhNE8&index=2
16	IV	Parallel RLC tank	RFIC	https://www.youtube.com/watch?v=Nb8NHHclch4&list=PLbMVogVj5nJQdGDSx243YPnNeLMBrhNE8&index=5
17	II	Mosfets	EDC	https://www.youtube.com/watch?v=MuBiC9yz2fc
18	II	Rectifier	EDC	https://www.youtube.com/watch?v=ofW4DZH83JQ
19	II	Boolean Algebra	STLD	https://www.youtube.com/watch?v=KergVtV3SxU
20	II	Latches and Filp-Flops	STLD	https://www.youtube.com/watch?v=jm0PGDSSBkI
21	II	Oscillators	ECA	https://www.youtube.com/watch?v=8iPRR6iCD8A
22	II	BJT & FET	ECA	https://www.youtube.com/watch?v=0C4uxtS-tlQ https://www.youtube.com/watch?v=4m49vM0Ryt8
23	II	Biot-Savart Law	EMTL	https://www.youtube.com/watch?v=4YEQ98NUWOY
24	II	Maxwell's Equations	EMTL	https://www.youtube.com/watch?v=2WiMeh1Dxi8


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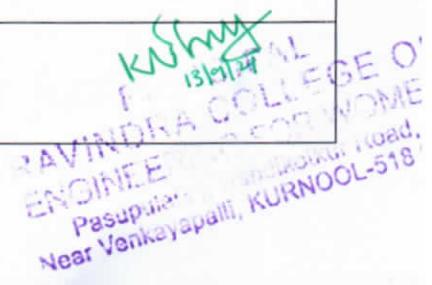
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Academic Year: 2016-17

DEPARTMENT OF CSE

Teachers Using ICT

S.No	Name Of The Teacher	Class	Topic Delivered using ICT	Subject	ICT tools Used
1	K.Shouryadhar	II CSE A & B	The Binomial Theorem	Discrete Mathematics	https://youtu.be/pQbPhZbpM10
2	K.Shouryadhar	II CSE A & B	Algebraic Structures	Discrete Mathematics	http://nptel.ac.in/courses/106106094/35
3	Dr. DHANARAJ CHEELU	III CSE A & B	Software Architecture	Software Engineering	https://nptel.ac.in/courses/106/101/106101061/
4	K.Mahesh Babu	III CSE A & B	DHTML	WT	https://youtu.be/HTFSIJALNKc
5	G.Fayaz Hussain	IV CSE A & B	Structural Pattern	Software Architecture & Design Patterns	https://youtu.be/lPsSL6_7NBg
6	K.Mahesh Babu	III CSE A & B	SERVLET LIFECYCLE	WT	PPT
7	T.Deepa	II CSE A & B	Database Architecture	Database Management Systems	http://nptel.ac.in/courses/106106093/1
8	T.Deepa	II CSE A & B	NORMAL FORMS	Database Management Systems	http://nptel.ac.in/courses/106106093/9
9	K.Shouryadhar	III CSE A & B	HADOOP FILE SYS	BDT	PPT
10	Dr. DHANARAJ CHEELU	III CSE A & B	White-Box Testing	SOFTWARE ENGINEERING	PPT
11	Y.Narasimhulu	III CSE A & B	Operating system structure	OPERATING SYSTEMS	http://nptel.ac.in/courses/106106144/2
12	Y.Narasimhulu	III CSE A & B	OS Security	OPERATING SYSTEMS	http://nptel.ac.in/courses/106106144/36
13	K. Shouryadhar	III CSE A & B	HADOOP ECO ECTURSYSTEURMS	BDT	PPT



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14	K. Vinod Kumar Reddy	II CSE A & B	DFA Minimization	FLAT	http://nptel.ac.in/courses/106104148/14
15	T. Deepa	II CSE A & B	INHERITANCE	OOP (JAVA)	PPT
16	Y.Narasimhulu	III CSE A & B	Deadlocks	OPERATING SYSTEMS	http://nptel.ac.in/courses/106106144/32
17	T. Deepa	II CSE A & B	APPLETS	OOP (JAVA)	PPT
18	K. Vinod Kumar Reddy	IV CSE A & B	Slicing, Methods	PYTHON	https://youtu.be/WWso6gVuKpI
19	K. Vinod Kumar Reddy	II CSE A & B	Turing Machine	FLAT	http://nptel.ac.in/courses/106104148/26
20	K. Vinod Kumar Reddy	IV CSE A & B	Python packages	PYTHON	PPT
21	K. Shouryadhar	IV CSE A & B	Android Developing Environment	MC	http://nptel.ac.in/courses/106106147/10
22	L.Devasena	III CSE A & B	Phases of a compiler	CD	http://nptel.ac.in/courses/106108113/1
23	K. Shouryadhar	IV CSE A & B	Fragments- Advanced Example	MC	http://nptel.ac.in/courses/106106147/26
24	Himabindhu	II CSE A & B	Hardwired Control	Computer organization	PPT
25	K. Shouryadhar	IV CSE A & B	Security and System Permissions	MC	http://nptel.ac.in/courses/106106147/30
26	G.Fayaz Hussain	IV CSE A & B	Behavioral Patterns	Software Architecture & Design Patterns	PPT
27	Himabindhu	II CSE A & B	DMA	Computer organization	PPT
28	L.Devasena	III CSE A & B	Run Time Environments	CD	http://nptel.ac.in/courses/106108113/21
29	K.Mahesh Babu	III CSE A & B	Unix structure	USP	https://youtu.be/Yo9ZtDeriMc
30	R Deepti C	III	TRANSACTION	STM	PPT

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	Rebeka	CSE A & B	FLOW		
31	S Vinod Kumar	IV CSE A & B	RTS Characteristics	RTS	http://nptel.ac.in/courses/106105036/2
32	G. Divya Zion	III CSE A & B	USE CASE DIAGRAMS	OOAD	PPT
33	R Deepti C Rebeka	III CSE A & B	FLOGRAPHS AND PATH TESTING	STM	PPT
34	S Vinod Kumar	IV CSE A & B	Modeling Timing Constraints	RTS	http://nptel.ac.in/courses/106105036/4
35	K.Mahesh Babu	III CSE A & B	Changing Positional parameters	USP	PPT
36	Prof.K. Srinivas	IV CSE A & B	OSI Security Architecture	CNS	https://youtu.be/UG26SS9pjwE
37	G. Divya Zion	III CSE A & B	INTERACTION DIAGRAMS	OOAD	https://youtu.be/Ba7SyM78cUM
38	Prof.K. Srinivas	IV CSE A & B	RSA Algorithm	CNS	http://nptel.ac.in/courses/106105031/28
39	Y. Inidira Priyadarshini	III CSE A & B	The ISO Model	CN	http://nptel.ac.in/courses/106105081/1
40	R.Deepthi Crestose Rebekah	IV CSE A & B	Screen Designing	HCI	PPT
41	G. Divya Zion	IV CSE A & B	: J2ME Architecture	MAD	PPT
42	Y. Inidira Priyadarshini	III CSE A & B	IPV4 Addresses.	CN	http://nptel.ac.in/courses/106105081/28
43	G. Divya Zion	IV CSE A & B	JDBC Objects	MAD	PPT
44	P. Ragavardhini	III CSE A & B	OLAP	DM	PPT
45	R.Deepthi Crestose Rebekah	IV CSE A & B	System menus	HCI	https://swayam.gov.in/nd1_noc20_cs45/...
46	P. Ragavardhini	III CSE	RULE BASED CLASSIFIER	DM	PPT

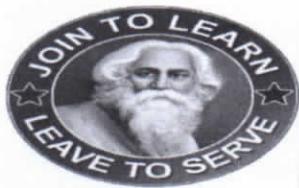
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		A & B			
47	Prof.K. Srinivas	IV CSE A & B	IDS	CNS	http://nptel.ac.in/courses/106105031/40

K.Srinivas
13/11/21

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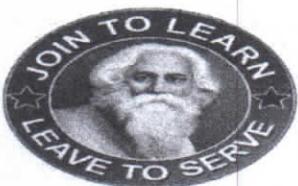
LIST OF ICT TOOLS

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

S.No	Class	Topic Delivered using ICT	Subject	ICT resource
1	III	Basic Maxwell's equations	AWP	https://www.youtube.com/watch?v=XUR-dnDa7eI
2	III	Antenna Arrays	AWP	https://www.youtube.com/watch?v=mudiXpU_DVQ
3	III	Pulse code modulation	DCS	https://www.youtube.com/watch?v=l_SqkcP6hZ4
4	III	Introduction Convolution Codes	DCS	https://www.youtube.com/watch?v=AnyVu5eDhAQ
5	III	FET	DSP	https://www.youtube.com/watch?v=r4Pc-rGBRJA
6	III	Discrete Time Signal & system	DSP	https://www.youtube.com/watch?v=8zbBJkw5KsM&list=PL4FA894BD6A9586E1
7	III	Logic Gates	VLSI	https://www.youtube.com/watch?v=fT3TC2AU_H8
8	III	Design of Adders	VLSI	https://www.youtube.com/watch?v=85XxQZqBNlg https://www.youtube.com/watch?v=YBY0k5mIQy0
9	IV	Photo Detector	OFC	https://www.youtube.com/watch?v=1X2Xt7wlcbA
10	IV	Attenuation & Power loss in fibers	OFC	https://www.youtube.com/watch?v=FVHzrGRN6i8
11	IV	Gunn diode, Varactor diode	MWE	https://www.youtube.com/results?search_query=Gunn+diode-principles+classes+by+iit+professors
12	IV	Directional Couplers	MWE	https://www.youtube.com/watch?v=44mBn7nyMIM
13	IV	Edge Detection	DIP	https://www.youtube.com/watch?v=9r8ph2pb9aw
14	IV	Discrete Cosine Transforms	DIP	https://www.youtube.com/watch?v=S8FkaEWfCOg
15	IV	Transmission media reflection	RFIC	https://www.youtube.com/watch?v=q9k9FPh0iJL&list=PLbMVogVj5nJQdGDSx243YPnNeLMBrhNE8&index=2
16	IV	RF system basic architectures	RFIC	https://www.youtube.com/watch?v=KUDGGsyh1Hs
17	II	Sample space & Events	PTSP	https://www.youtube.com/watch?v=leVm6xuKdlU
18	II	Random Variables	PTSP	https://www.youtube.com/watch?v=zufeSyREcQ4

Kishore
13th July 2016

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(An ISO 9001:2008 Certified Institution)

19	II	Zener Diode and applications	EDC	https://www.youtube.com/watch?v=I2nrBITEVls
20	II	BJT biasing- methods	EDC	https://www.youtube.com/watch?v=l6M6FvjUdTI
21	II	Vestigial sideband (VSB) modulator	ACS	https://www.youtube.com/watch?v=KC8kh5FaIc0
22	II	Amplitude modulation	ACS	https://www.youtube.com/watch?v=UznnkHMisIk
23	II	Transient response of second order systems	CSE	https://www.youtube.com/watch?v=RJaQkFE3HQA
24	II	Mason's gain formula	CSE	https://www.youtube.com/watch?v=au7rDksP5nQ

L Shiva
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