



RAVINDRA COLLEGE OF ENGINEERING FOR WOMEN

Permanently Affiliated to JNTUA, Anantapuramu & Recognized by UGC u/s 2(f) & 12(B)
Near Venkayapalle, Pasupula Village, Nandikotkur Road, Kurnool – 518452
Andhra Pradesh – India

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CAO ASSIGNMENT QUESTIONS

UNIT – V

1. Discuss the function of TLB with neat sketch.
2. Explain in detail Direct Memory Access (DMA).
3. Explain the basic concepts of virtual and cache memory techniques.
4. Draw and explain block diagram of associative memory
5. Briefly discuss about modes of transfer.